

CHIP WITH MEASURING RELIABILITY AND A METHOD THEREOF

ABSTRACT OF THE INVENTION

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A chip with measuring reliability and a method thereof are provided. The present invention serially connects a resistor having a resistance equal to or a little more than a maximum resistance of the chip itself to the resistor R_s of the chip so as to compensate the resistance differences among chips. A noise to signal (N/S) ratio of the
10 chip is decreased, and a measuring reliability of the chip is improved.

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